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### NEW QUESTION: 1

A network engineering team is designing a lab network for a customer demonstration. The design engineer wants to show that the resiliency of the MPLS Traffic Engineering Fast Reroute solution has the same failover Tailback times as a traditional SONET/SDH network (around 50 msec). Which type of MPLS TE tunnel must be considered for this demonstration to address the link failure and node failure within the lab network?

- A. FRR backup tunnel
- B. TE backup tunnel
- C. next-hop tunnel
- D. next-next-hop tunnel

**Answer: D**

### NEW QUESTION: 2

Salesforce is which of the following:

- A. Marketing Management
- B. Customer Support Management
- C. Campaign Management
- D. Salesforce Automation

**Answer: A,B,D**

### NEW QUESTION: 3

左側のメモリタイプをクリックしてドラッグし、右側の説明と一致させます。

#### Answer:

Explanation:

Explanation:

#### \* RDIMM

RDIMMs add a register, which buffers the address and command signals. The integrated memory controller in the CPU sees the register instead of addressing the memory chips directly.

#### \* LRDIMM

Load Reduced DIMMs replace the register with an Isolation Memory Buffer (iMB by Inphi) component. The iMB buffers the Command, Address, and data signals. The iMB isolates all electrical loading (including the data signals) of the memory chips on the (LR)DIMM from the host memory controller.

#### \* UDIMM

UDIMM (unregistered memory) is a type of memory chip used mostly in desktop and laptop computers. More commonly called unbuffered memory, UDIMMs run faster than registered memory (RDIMM) and cost less, but are not quite as stable as RDIMMs.

#### \* NVDIMM

A Non-Volatile Dual In-line Memory Module (NVDIMM) is a computer memory RAM DIMM that retains data even when electrical power is removed either from an unexpected power loss, system crash or from a normal system shutdown. NVDIMMs can be used to improve application performance, data security, system crash recovery time and to enhance SSD endurance and reliability.

参照：<http://www.anandtech.com/show/6068/lrdimms-rdimms-supermicros-latest-twin/2>

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